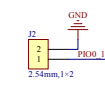
[illegible]

ISP



The image shows two pin diagrams for the 6X2 modules. The top module, labeled J7, has pins 1 through 12. Pins 1, 2, 11, and 12 are connected to GND. Pin 3 is connected to DP3V3. Pin 4 is connected to ESDI. Pin 5 is connected to ECLK. Pin 6 is connected to ECLK. Pin 7 is connected to ESDIO. Pin 8 is connected to ESDIO. Pin 9 is connected to ERST. Pin 10 is connected to GND. Pin 11 is connected to RF VCC. Pin 12 is connected to RF MOSI. The bottom module, labeled J9, has pins 1 through 12. Pins 1, 2, 11, and 12 are connected to GND. Pin 3 is connected to EIO0. Pin 4 is connected to EIO1. Pin 5 is connected to EIO2. Pin 6 is connected to EIO3. Pin 7 is connected to EIO5. Pin 8 is connected to ESEL. Pin 9 is connected to RF DIO0. Pin 10 is connected to RF DIO1. Pin 11 is connected to RF DIO2. Pin 12 is connected to RF DIO3. Pin 13 is connected to RF DIO5. Pin 14 is connected to RF SEL.

[illegible]

Title		
Size A2	Number	Revision
Date: 2016/7/12	Sheet of 172M433SV-1 (JEMC) Rev. 00000000	